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☐ 1. **A single-V/sub t/ low-leakage gated-ground cache for deep submicron**
Agarwal, A.; Hai Li; Roy, K.;
Solid-State Circuits, IEEE Journal of
Volume 38, Issue 2, Feb. 2003 Page(s):319 - 328
Digital Object Identifier 10.1109/JSSC.2002.807414

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☐ 2. **A fast high-resolution track trigger for the H1 experiment**
Naumann, J.; Baird, A.; Elsen, E.; Fleming, Y.H.; Kolander, M.; Kolya, S.; Meer, D.; Mercer, D.; Newman, P.R.; Sankey, D.; Schoning, A.; Schultz-Coulon, H.-C.; Wissing, C.;
Nuclear Science, IEEE Transactions on
Volume 48, Issue 4, Part 1, Aug. 2001 Page(s):1276 - 1281
Digital Object Identifier 10.1109/23.958765

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